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ISSUE CLASSIFICATION	
Class	Subclass

**U.S. UTILITY Patent Application**

<p>FD O.I.P.E.</p> <p>SCANNED <u>301</u> Q.A. <u>CA</u></p>	PATENT DATE
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APPLICATION NO. 09/751674	CONT/PRIOR	CLASS 712	SUBCLASS 204	ART UNIT 2183	EXAMINER Chen, Felli Li
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## APPLICANTS

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### **अनुसंधान**

Circuit and method for instruction compression and dispersal in wide-issue processors

PTO-2040  
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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